

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application for:

Guillou et al.

Application No.: 10/587,460

Patent No.: 7,680,271

For: ZERO-KNOWLEDGE PROOF
CRYPTOGRAPHY METHODS AND
DEVICES

Examiner: Abrishamkar, Kaveh

Art Unit: 2431

Filing Date: July 24, 2006

Confirmation No.: 2714

Mail Stop Certificate of Corrections Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION

Dear Commissioner:

Upon review of the above-referenced Letters Patent, errors were noted. At least one of the noted errors was not the mistake of the U.S. Patent and Trademark Office. The error(s) occurred in good faith. The payment of the fee set forth in 37 CFR 1.20(a) is included herewith.

Please correct the following:

Column 11, Claim 6

Line 66, "in challenges challenges d_1, d_2, \dots, d_m " should read --m challenges

d_1, d_2, \dots, d_m --

The correct text was provided in the Amendment and Response to Office action submitted on August 11, 2009, which amended claim 6.

Column 12, Claim 6

Line 4, “to the controller, and” should read --to the processor-implemented controller, and--

The correct text was provided in the Amendment and Response to Office action submitted on August 11, 2009, which amended claim 6.

Column 12, Claim 8

Line 15, “chooses at random in integers” should read --chooses at random m integers--

The correct text was provided in the Amendment and Response to Office action submitted on August 11, 2009, which amended claim 8.

Column 12, Claim 8

Line 15, line 19 “producing a word of m bits” should read --producing a word of m bits--

The correct text was provided in the Amendment and Response to Office action submitted on August 11, 2009, which amended claim 8.

Column 12, Claim 13

Line 63, “or the relationship $G_i \times Q_i^V = 1 \bmod n$ ” should read --or the relationship $G_i \times Q_i^V = 1 \bmod n$ --

The correct text was provided in the Amendment and Response to Office action submitted on August 11, 2009, which amended claim 13.

Column 13, Claim 13

Line 18, “to any of claims 5-8 claim 6” should read --to claim 6--

Claim 13 was amended by the Amendment and Response to Office action submitted on August 11, 2009. Prior to the aforementioned amendment, claim 13 recited "... a method according to any of claims 5-8." The aforementioned amendment added the phrase "claim 6" without deleting the phrase "any of claims 5-8[.]"

Column 14, Claim 19

Line 17, "arranging, by processor," should read --arranging, by the processor--

The correct text was provided in the Amendment and Response to Office action submitted on August 11, 2009, which amended claim 20, (now issued as claim 19).

Column 14, Claim 19

Line 22, "arranging by processor, each public key G_i (where $i = 1, \dots, m$ to" should read -- arranging by the processor, each public key G_i (where $i = 1, \dots, m$) to--

The correct text was provided in the Amendment and Response to Office action submitted on August 11, 2009 which amended claim 20, (now issued as claim 19).

It is respectfully requested that a Certificate of Correction be issued. The Commissioner is hereby authorized to charge shortages or credit overpayments to Deposit Account No. 500393.

Respectfully submitted,
SCHWABE, WILLIAMSON & WYATT, P.C.

Dated: Jan. 11, 2011

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO. : 7,680,271

APPLICATION NO. : 10/587,460

ISSUE DATE : March 16, 2010

INVENTOR(S) : Louis Guillou and Jean-Jaques Quisquater

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 11, Line 66, "in challenges d_1, d_2, \dots, d_m " should read --m challenges d_1, d_2, \dots, d_m --

Column 12, Line 4, "to the controller, and" should read --to the processor-implemented controller, and--

Column 12, Line 15, "chooses at random in integers" should read --chooses at random m integers--

Column 12, Line 19, "producing a word of in bits" should read --producing a word of m bits--

Column 12, Line 63, "or the relationship $G_i \times Q_{iv} = 1 \pmod n$ " should read --or the relationship $G_i \times Q_{iv} = 1 \pmod n$,--

Column 13, Line 18, "to any of claims 5-8 claim 6" should read --to claim 6--

Column 14, Line 17, "arranging, by processor," should read --arranging, by the processor--

Column 14, Line 22, "arranging by processor, each public key G_i (where $i = 1, \dots, m$) to" should read -- arranging by the processor, each public key G_i (where $i = 1, \dots, m$) to--

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: **Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**